

CLAIMS

1. A demodulator in a digital broadcasting receiver for receiving digital broadcasting by layered transmission system, characterized by comprising pseudo-data inserting means for inserting pseudo-data for decoding a TMCC signal immediately before and/or immediately after said TMCC signal.

2. The demodulator according to Claim 1, characterized in that said decoding is Viterbi decode.

3. The demodulator according to Claim 1, characterized in that said pseudo-data includes at least related fixed data maintaining a convoluted time sequential relationship.

4. The demodulator according to Claim 3, characterized in that said pseudo-data further includes fixed data and a synchronization word for frame identification.

5. The demodulator according to Claim 1, characterized in that said demodulator comprises a pseudo-data generator for generating said pseudo-data.

6. The demodulator according to Claim 1, characterized in that said pseudo-data inserting means comprises a serial/parallel converter for serial/parallel converting a base band signal demodulated by a demodulation circuit, a pseudo-data generator for generating said pseudo-data, and a selector for selecting output data of said serial/parallel converter and said pseudo-data outputted from said pseudo-data generator.

7. The demodulator according to Claim 7, characterized in that said demodulator has an accumulator for writing output data of

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a serial/parallel converter at a half rate of a transmission rate and reading out at the same rate as said transmission rate to output to said selector.

- 5 8. A method of demodulation for a digital broadcasting receiver for receiving digital broadcasting by layered transmission system, characterized by comprising pseudo-data inserting means for inserting pseudo-data for decoding a TMCC signal immediately before and/or immediately after said TMCC signal.

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